

FIGURE 1

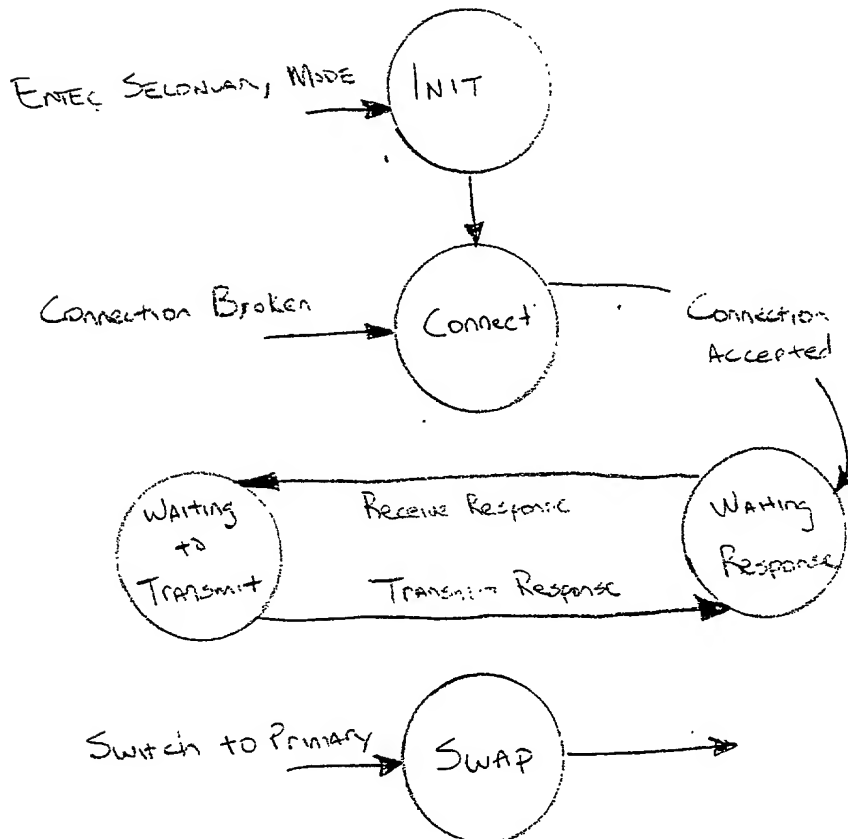


FIGURE 2

10067888.030002

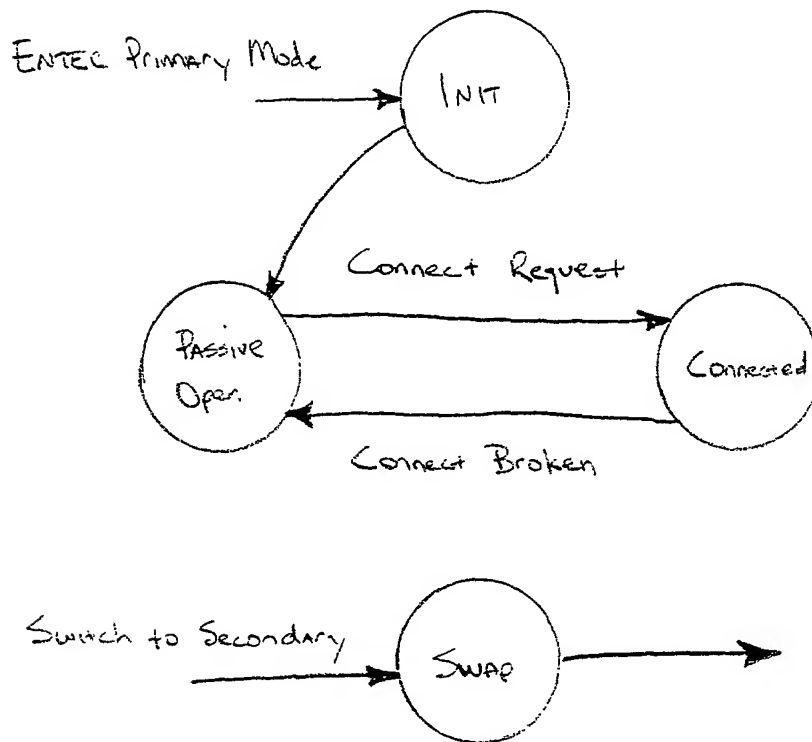


FIGURE 3

10037888-030202

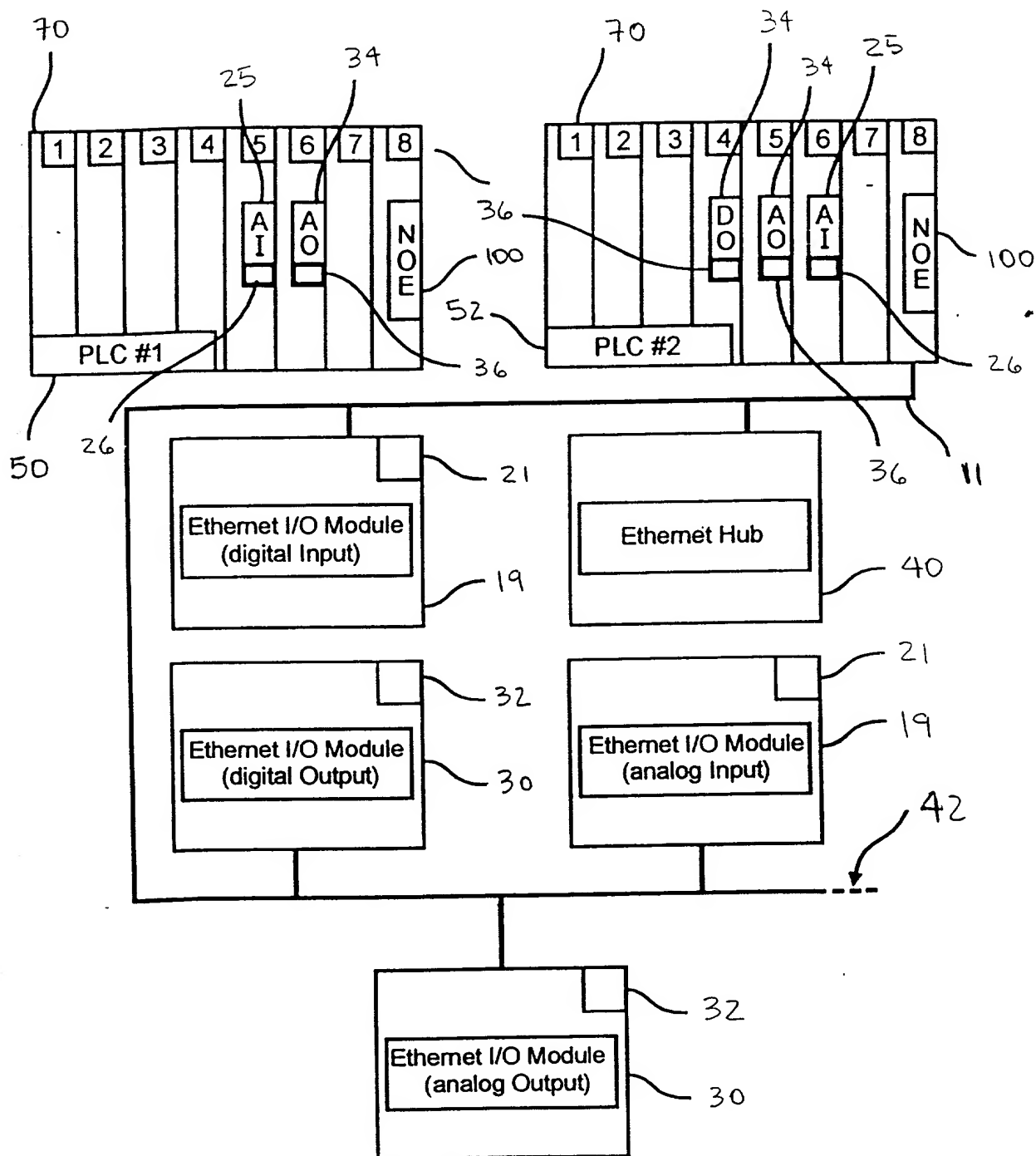
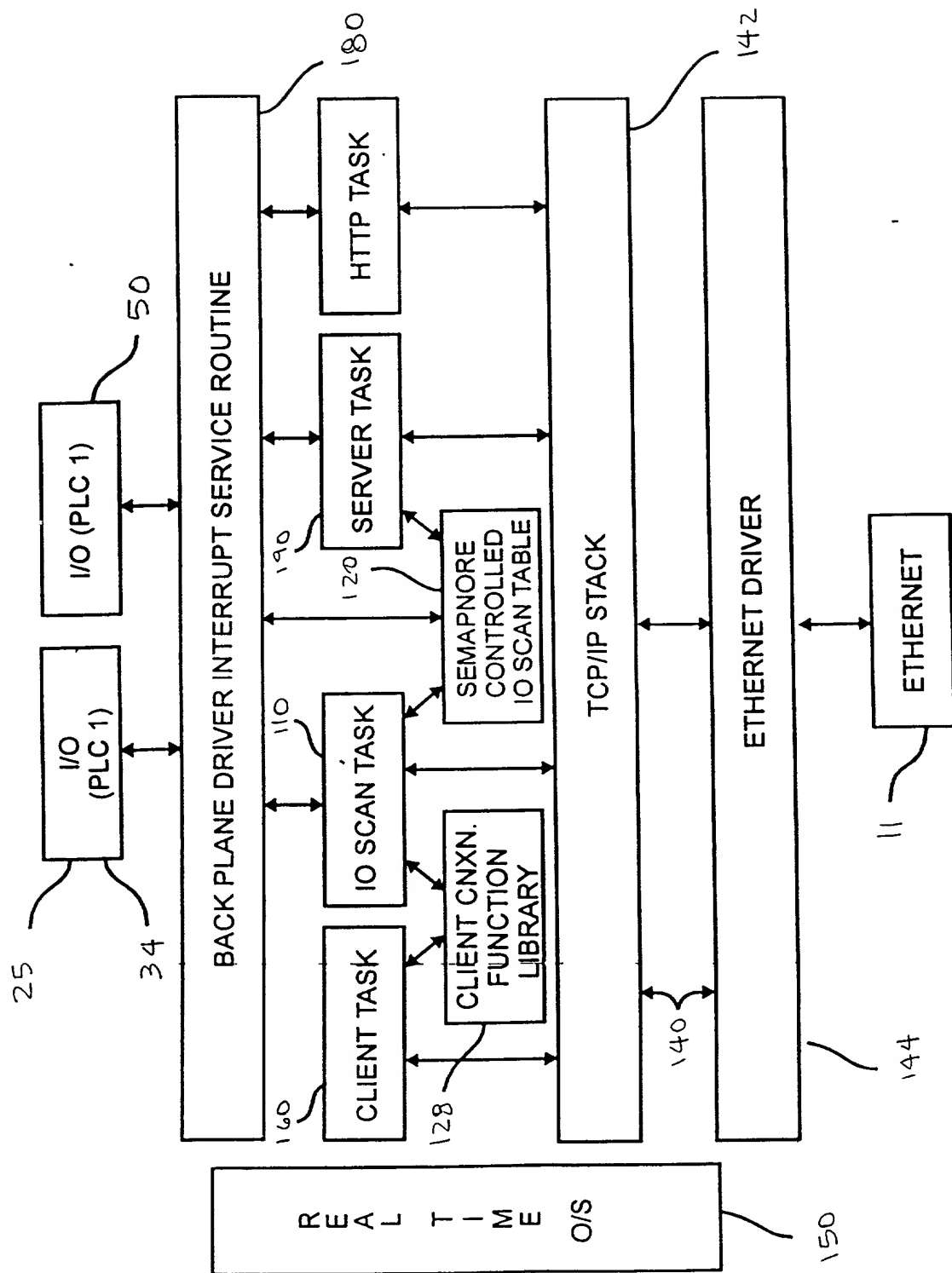


Fig. 4



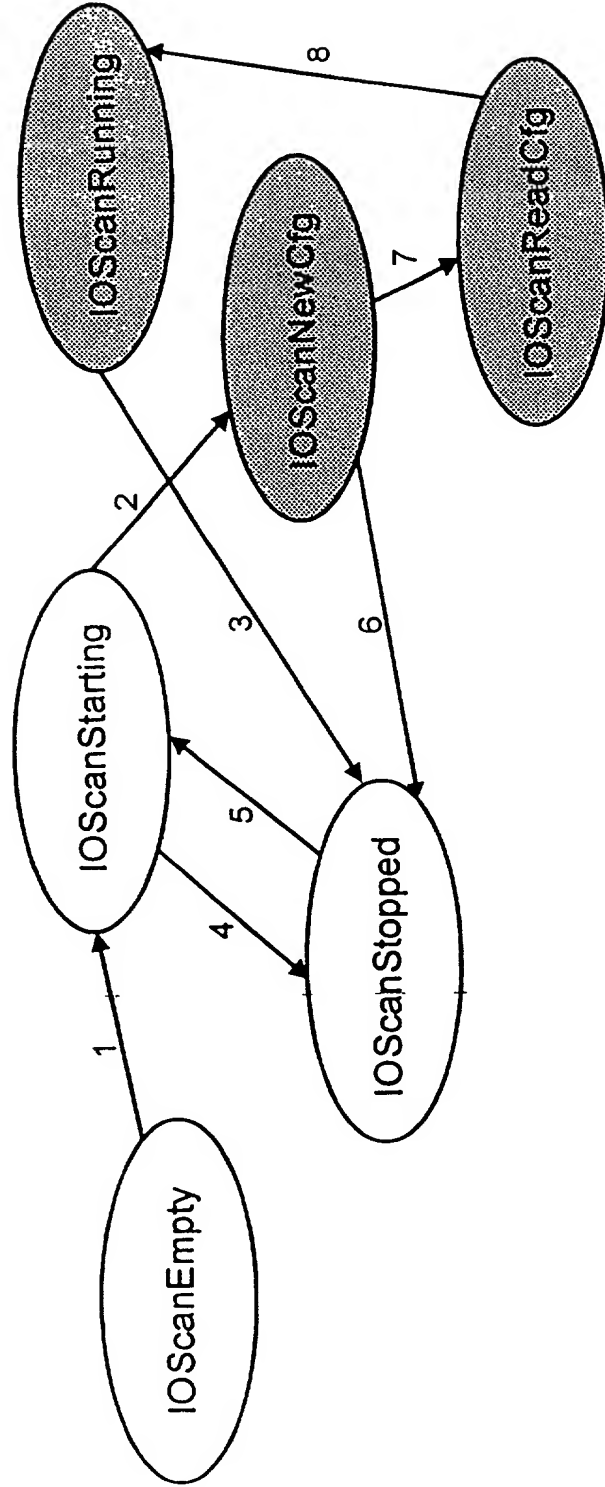


Fig. 6

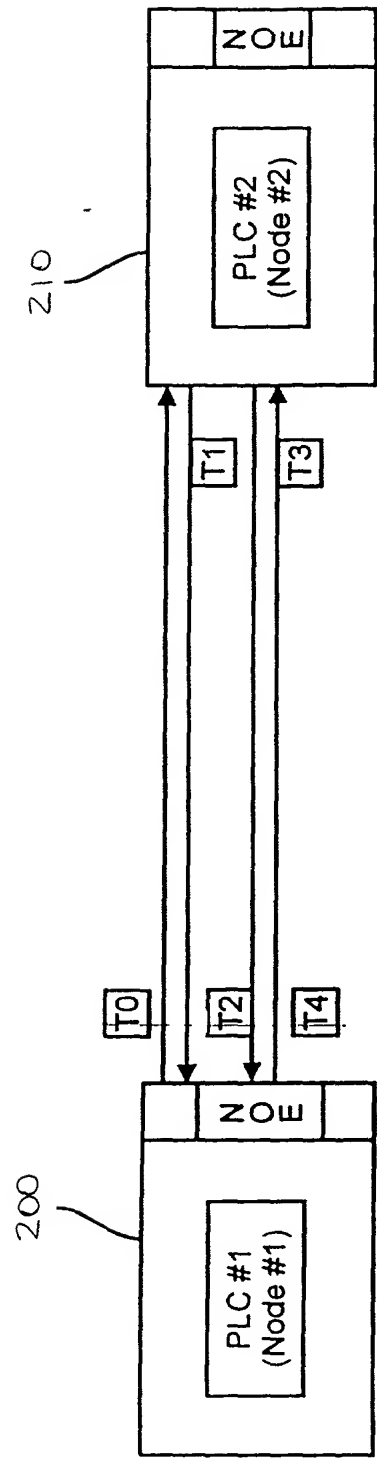


Fig. 7

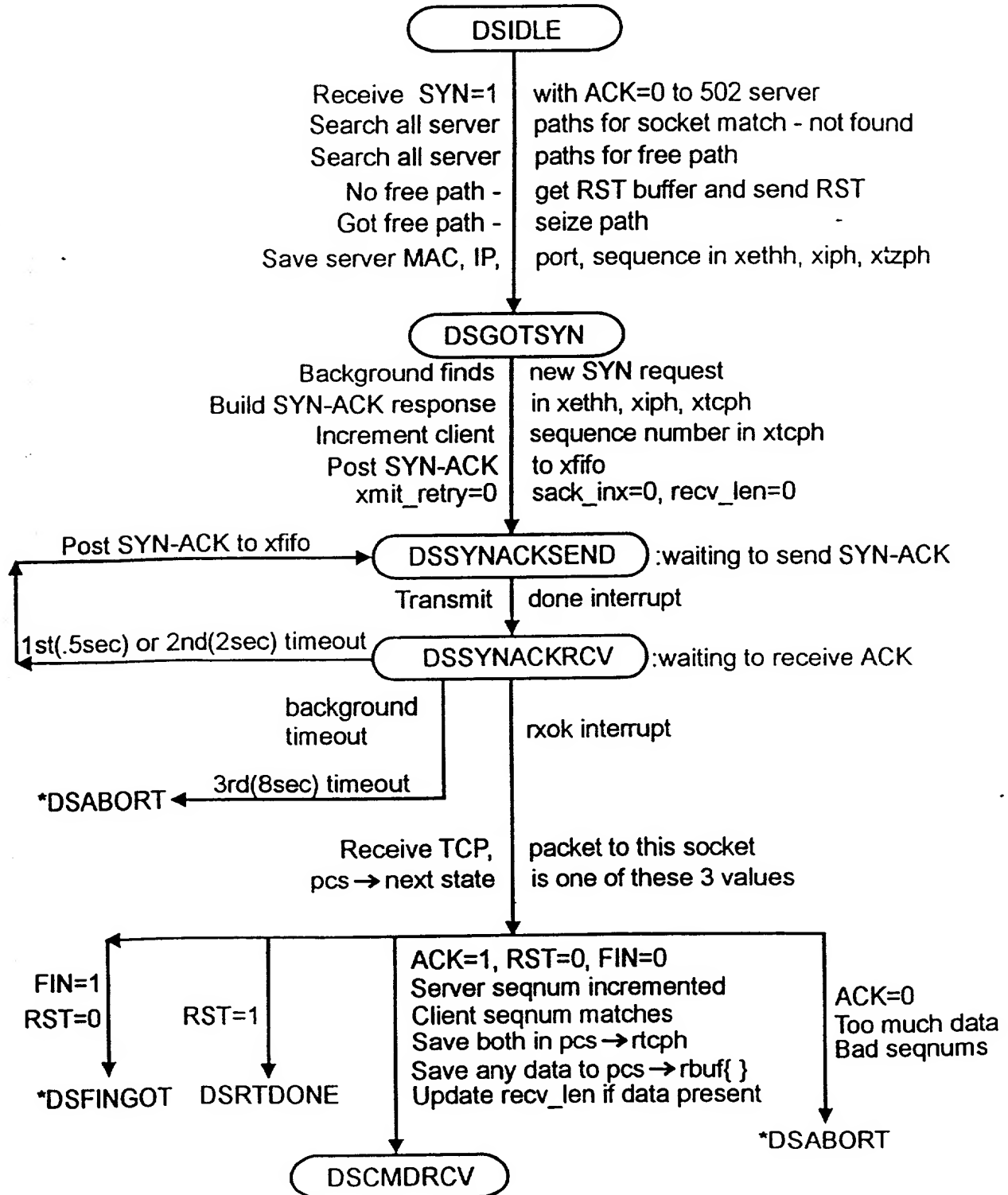


Fig. 8A

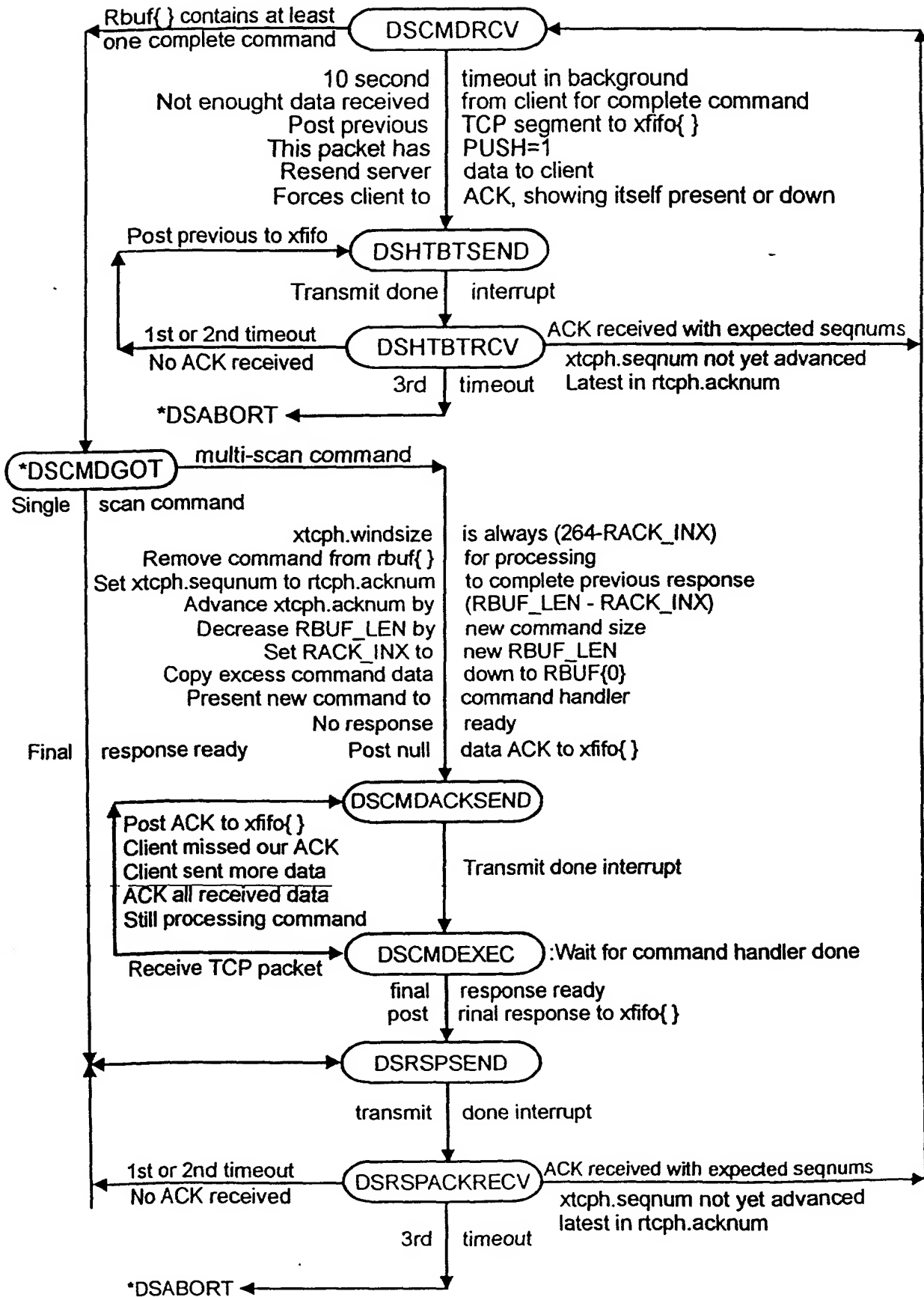


Fig. 8B

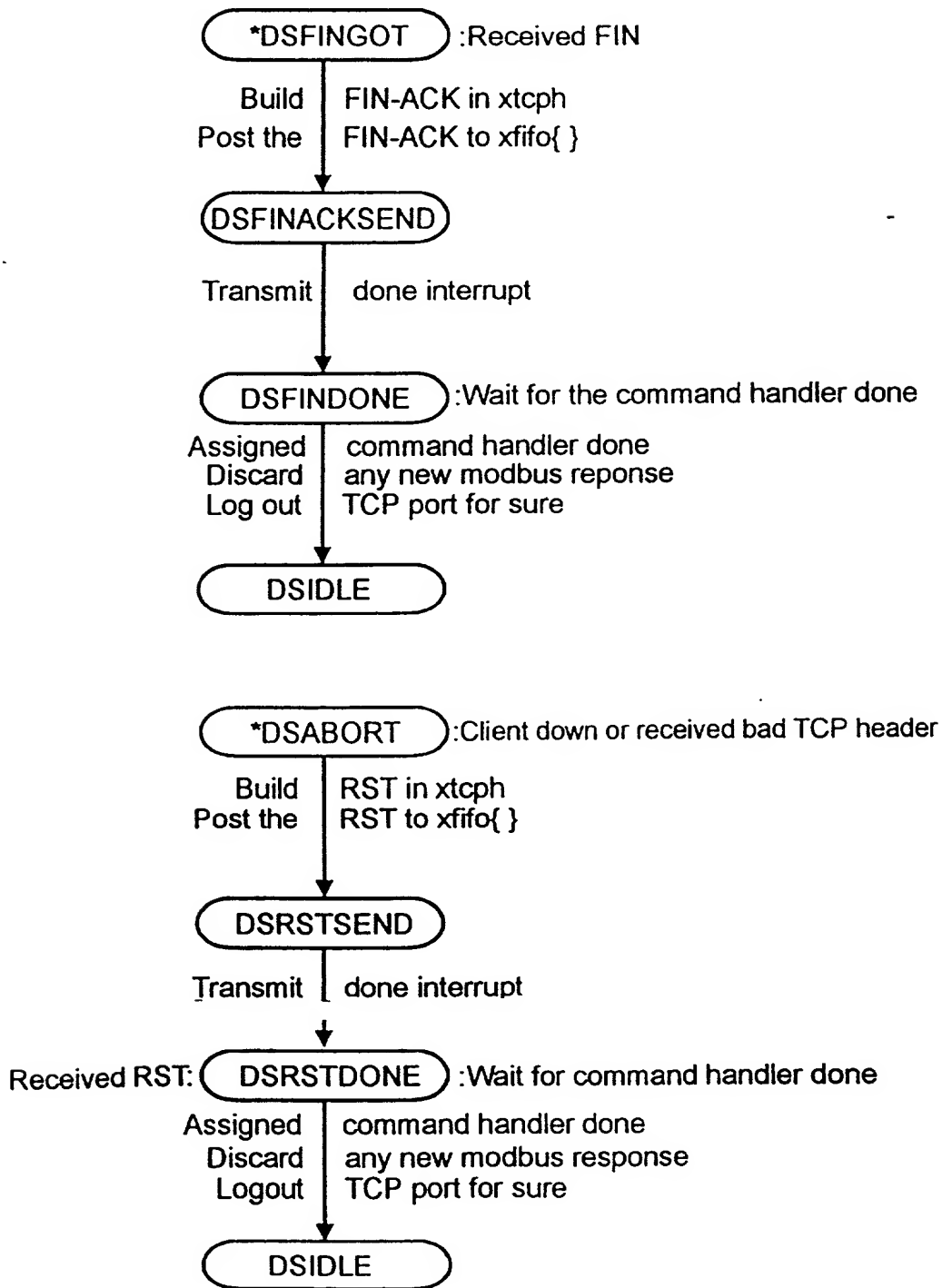


Fig. 8C

pcs→ Path state: state machine for idle data master path.
Common for MSTR element and cyclic transactions

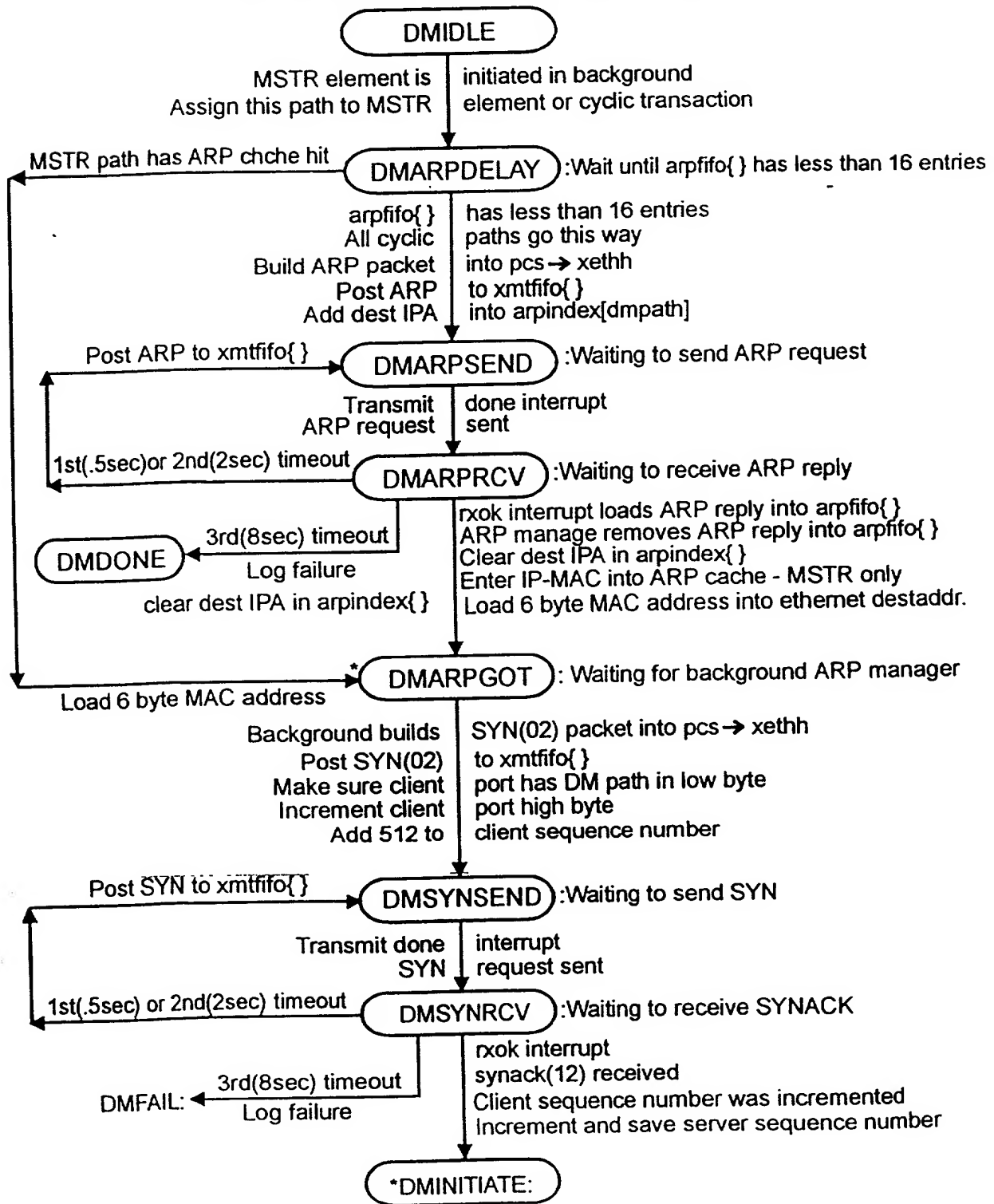
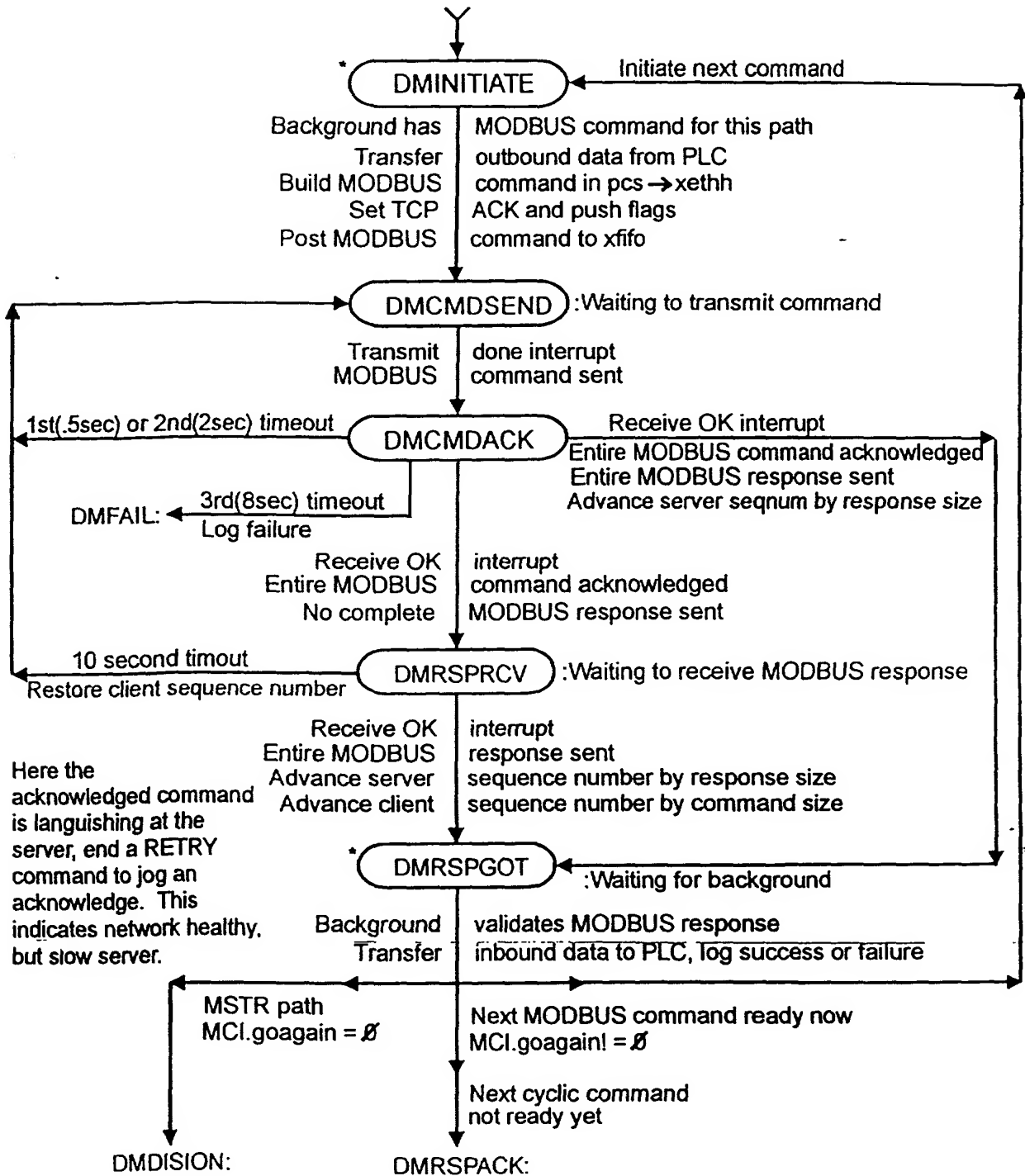


Fig. 8D



* - resides in this state for Ø time
RCV interrupt sets next state to these values

Fig. 8E

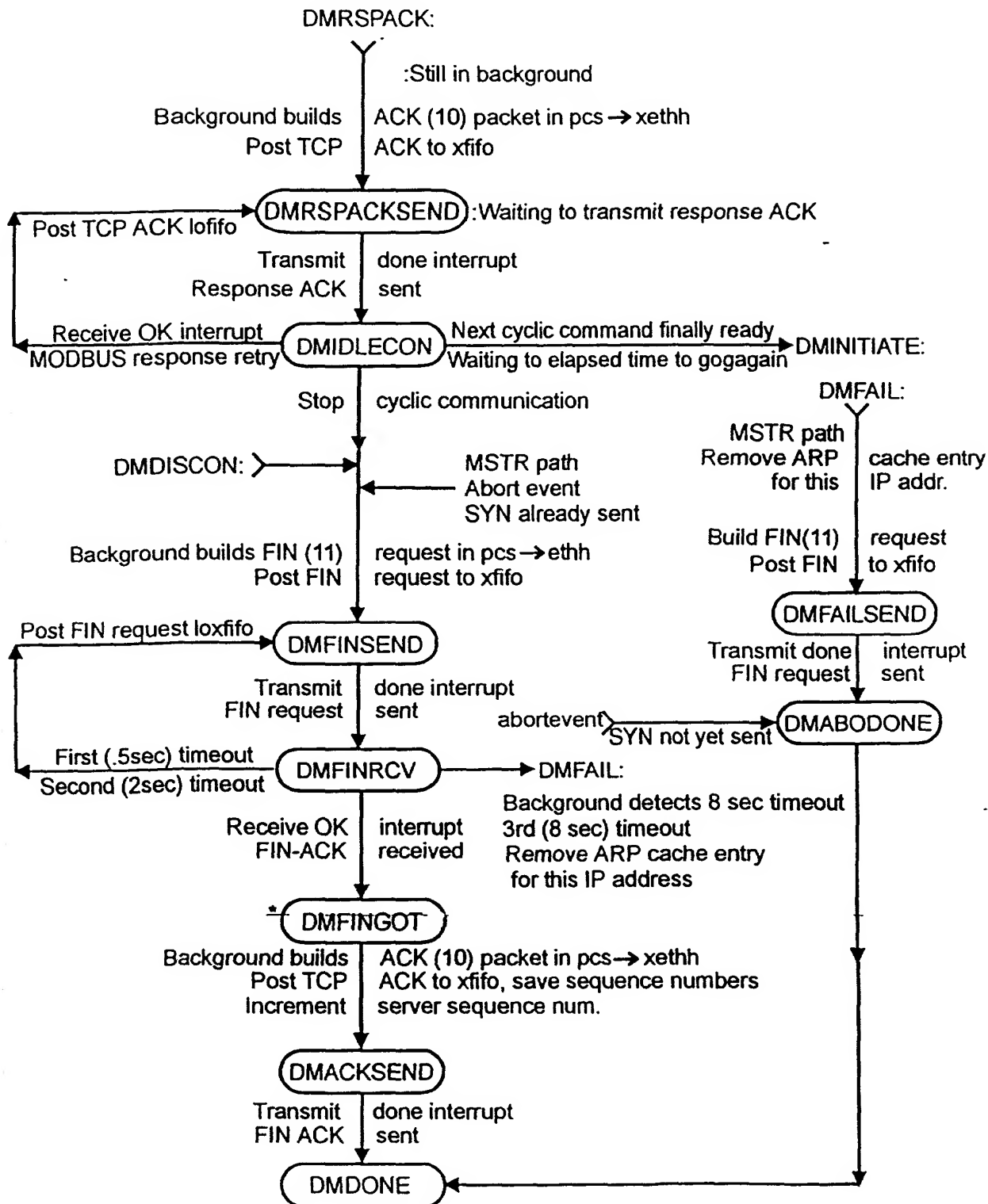


Fig. 8F

pcs → xmit_state state machine: once a path has been submitted into the xfifo{ }, it cannot be removed. But the background can abort the packet, as long as it is not in the XMTBID or XMTSEND state. These states should not persist.

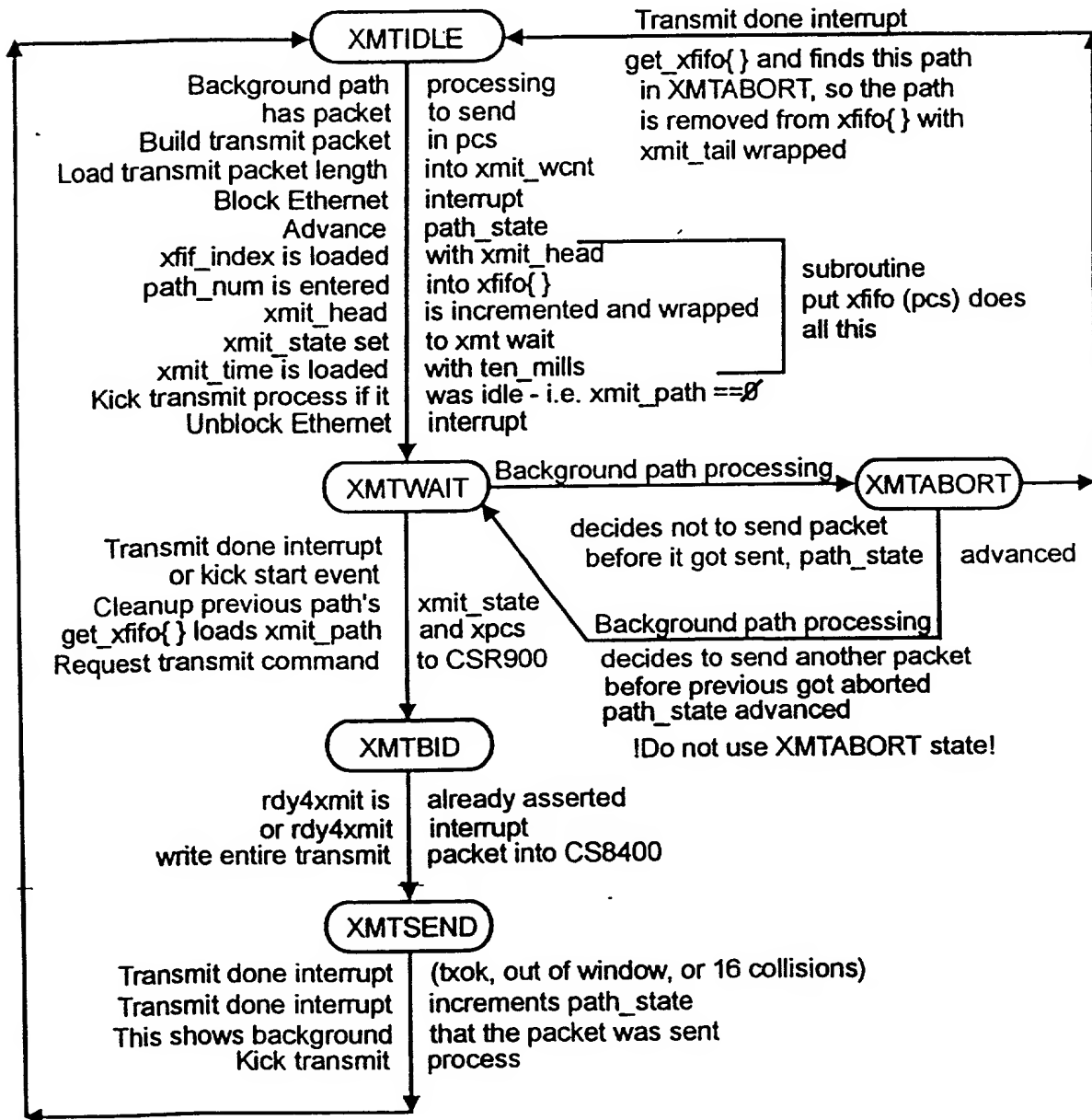


Fig. 86